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01/03/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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|----------------------|---------------------------|--------------|-----------------|-------------|------------------------------|
| APPL NUM 10034827 | FILING DATE 01/03/2002 | CLASS 381 | SUBCLASS 257 | GAU 2835 | EXAMINER TA/Hatt Mitchell |
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****APPLICANTS:** Morrison Gary; Edwards Darwin; Stark Leslie;

****CONTINUING DATA VERIFIED:**

THIS APPLN CLAIMS BENEFIT OF 60/258,525 12/28/2000

**** FOREIGN APPLICATIONS VERIFIED:**

| | | | |
|--|---|----------------------------------|--------------------------------|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> | ATTORNEY DOCKET NO TI-31373 |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no | | | |
| 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no | | | |
| Verified and Acknowledged Examiners's initials | | | |
| TITLE : Chip-scale packages stacked on folded interconnector for vertical assembly on substrates | | | |

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L/Rev. 12-94

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|---|-----------|---------------------------|----------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. |
| | | Print Fig. | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Application Examiner | |
| | | PREPARED FOR ISSUE | |
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